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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/719,262	11/24/2003	Kewal K. Chopra	TA-3113	5698
29322	7590	07/31/2006	EXAMINER BAUER, SCOTT ALLEN	
U.S. ARMY TACOM ATTN: AMSTA-LP/281 6501 E. 11 MILE RD. WARREN, MI 48397-5000			ART UNIT 2836	PAPER NUMBER

DATE MAILED: 07/31/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/719,262

Applicant(s)

CHOPRA, KEWAL K.

Examiner

Scott Bauer

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-9 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 24 November 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____.

DETAILED ACTION***Claim Objections***

1. Claim 7 recites the limitation "the initial design parameters" in line 1. There is insufficient antecedent basis for this limitation in the claim.

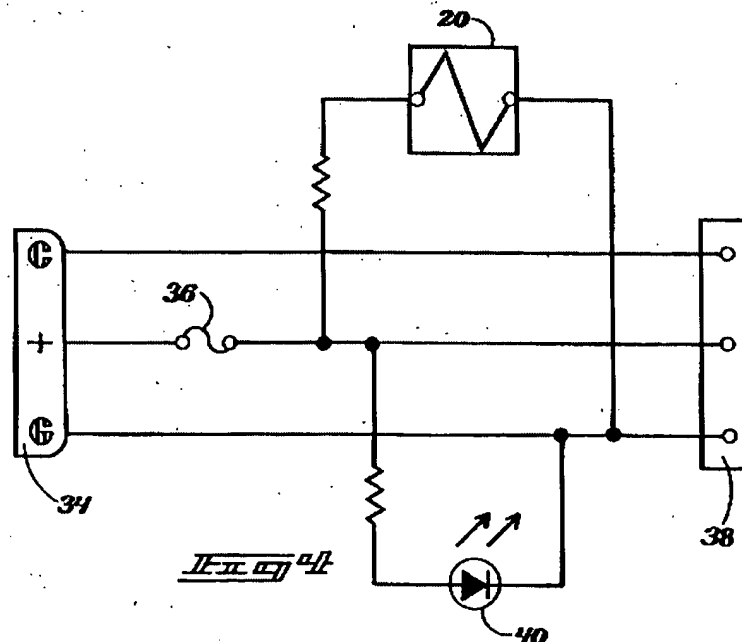
Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1, 2, 4-7 & 9 are rejected under 35 U.S.C. 102(b) as being anticipated by Jarred (US 4,887,975).



4. With regard to Claim 1, Jarred, in Figure 4, discloses an indicator circuit for continuously verifying status of an electrical protection device (36), said device being energized by a power source (connected to 34) and series connected between said power source and an electrical load (connected to 38) to thereby form an electrical circuit, wherein said indicator circuit comprises a series resistor, a current responsive light emitting diode (40) , and a ground (labeled "G" on terminal 34), which are series arranged in that order and electrically connected to said formed electrical circuit at a junction between said electrical protection device and said electrical load.

5. With regard to Claim 2, Jarred, discloses the circuit of claim 1 wherein said power source has alternating current (column 4 lines 17-19).

6. With regard to Claim 4, Jarred discloses the circuit of claim 1 wherein said light emitting diode would inherently provide sufficient current to emit visible light by pre-selection of said resistor based upon voltage of the power source (column 4 lines 46-49).

7. With regard to Claim 5, Jarred discloses the circuit of claim 1 wherein said light emitting diode is continuously illuminated if the electric protection device is intact and performing its intended function. (column 4, lines 46-49).

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8. With regard to Claim 6, Jarred discloses The circuit of claim 1 wherein said light emitting diode is not continuously illuminated if the electric protection device is open and not performing its intended function (column 4, lines 46-49).

9. With regard to Claim 7, Jarred discloses the circuit of claim 1 which inherently does not modify the initial design parameters for the electrical load within the electrical circuit.

10. With regard to Claim 9, Jarred discloses an indicator circuit for continuously monitoring status of an electrical circuit having an electrical protection device (36) therein to perform protective functions, comprises: a. said electrical protection device (36) being series connected between a power source (connected to 34) and at least one electrical load (connected to 38); b. said indicator circuit having an electrical interconnection to said electrical circuit at a junction between said electrical protection device and said load; and c. said indicator circuit including a resistor, a light emitting diode (40), and a permanent ground in series connection so current will continuously pass through said electrical circuit from the power source, to electrical protection device, to load, and simultaneously to said indicator circuit via said resistor, light emitting diode, and ground causing said diode to illuminate which will signal that said electrical protection device is intact and performing its intended function. (column 4, lines 46-49 & 65-68) & column 5 lines 1-2).

Claim Rejections - 35 USC § 103

11. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

12. Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Jarred (US 4,887,975) in view of Prager et al. (US 4,215,386).

13. With regard to Claim 3, Jarred teaches the circuit of Claim 1 wherein said power source has alternating current.

Jarred does not teach that the power source has direct current.

Prager et al., teaches that fuse with a blown fuse indicator can be used to protect circuits connected to both AC and DC sources (column 5 lines 29-42).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Jarred with Prager et al., by Connecting a DC source to the circuit of Jarred instead of an AC source, for the purpose of allowing the circuit to provide blown fuse indication for loads that require DC as well as AC thus increasing the robustness of the circuit.

14. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Jarred et al. (US 4,887,975) in view of Nasu et al. (US 4,281,322).

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15. With regard to Claim 8, Jarred teaches the circuit of Claim 1 which could necessarily be provided for electrical circuits of industrial sites, homes, businesses, and ground, air, and water transports.

Jarred does not teach a complex electrical system comprised of a multiplicity of status indicator circuits.

Nasu et al., in Figure 2, teaches a fuse blowing detector comprising a complex electrical system comprised of a multiplicity of status indicator circuits.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Jarred with Nasu et al., by using a multiplicity of the status indicator circuits of Jarred in a system as taught by Nasu et al., for the purpose of providing continuous blown fuse indication in a system comprising a plurality of fuses.

Conclusion

16. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Scott Bauer whose telephone number is 571-272-5986. The examiner can normally be reached on M-F 9am-6pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on 571-272-2058. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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CHAU N. NGUYEN
PRIMARY EXAMINER